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APPLICANTS

Shuji Ikeda, Tokyo, JAPAN;

Yasuko Yoshida, Sayama-shi, JAPAN;
Masayuki Kojima, Tokyo, JAPAN; Kenji Shiozawa, Hidaka-shi, JAPAN;
Mitsuyuki Kimura, Tokyo, JAPAN;
Norio Nakagawa, Tokyo, JAPAN;
Koichiro Ishibashi, Warabi-shi, JAPAN;
Yasuhisa Shimazaki, Tokyo, JAPAN;
Kenichi Osada, Tokyo, JAPAN;
Kunio Uchiyama, Tokyo, JAPAN;

** CONTINUING DATA ^{Yes} ^{DV} *****

This application is a CON of 10/379,543 03/06/2003 PAT 6,753,231
which is a CON of 10/157,978 05/31/2002 PAT 6,559,006
which is a CON of 09/372,007 08/11/1999 PAT 6,436,753

** FOREIGN APPLICATIONS ^{Yes} ^{DV} *****

JAPAN 10-226663 08/11/1998

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 06/18/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <u>Richard</u> Examiner's Signature	JAPAN	20	10	4
<u>DV</u> Initials				

ADDRESS

020457
ANTONELLI, TERRY, STOUT & KRAUS, LLP
1300 NORTH SEVENTEENTH STREET
SUITE 1800
ARLINGTON, VA
22209-9889

TITLE

Semiconductor integrated circuit and method for manufacturing the same

1 of 2 FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.16 Fees (Drawing)	12/24/04 11:50 AM
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